

PATENT NUMBER

U.S. **UTILITY** Patent Application

<p>O.I.P.E.</p> <p>AK SCANNED</p> <p>SBA</p> <p>O.A.</p>	<p>PATENT DATE</p>
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APPLICATION NO. 09/678175	CONT/PRIOR	CLASS 918 375	SUBCLASS 1	ART UNIT 281 2825	EXAMINER Russo/SLC
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NTS Victor Konrad
Vivek Joshi

APPLICANTS

अनुप्रास

Method to reduce the power consumption of large plas by clock gating guided by recursive shannon decomposition of the and-plane

PTO-2040
12/99

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<input type="checkbox"/> TERMINAL DISCLAIMER	DRAWINGS		CLAIMS ALLOWED	
	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims
<input type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed.	_____ (Assistant Examiner)		NOTICE OF ALLOWANCE MAILED _____ _____ _____	
<input type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S Patent. No. _____ _____ _____	_____ (Primary Examiner)		ISSUE FEE Amount Due: _____ Date Paid: _____	
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